

Date: **July, 1989**

Subject: **FIRMWARE UPGRADE—P4.02.01.5
(CPU) / P1.01.01.3 (LNT)**

Model: **APR-5003V**

Serial No: **10,001-10,520**

DESCRIPTION

Firmware version P4.02.01.5 (CPU) / P1.01.01.3 (LNT) for the APR-5003V is now available. The three EPROMs to be replaced are IC13 and IC14 on the CPU Board and IC13 on the LNT Board.

NOTE: The new firmware is not compatible for use with the APR-5003 or other APR-5000 series machines.

The new version firmware is factory installed in APR-5003V units with serial numbers 10,521 and higher.

IMPROVEMENTS AND FEATURES

- Pressing the PLAY switch after CHASE is initiated from "Resolve on Play" will no longer cause the machine to enter a non-performing resolve mode.
- If Establish and Maintain triggered operations are requested in manner where the source is changed, the second request of the triggered operation will fail. For example:
 - 1) From Establish LTC to Maintain with video, tone, or VITC
 - 2) From Establish VITC to Maintain with LTC.

- Operation of offset capture relative to an unlocked moving master or to frozen (and repeating) master time code is corrected.
- Loading a non-zero value for the manual acceleration allowance (memory location 50) will override any internal acceleration allowance defaults. When a zero value is loaded, the internal default acceleration allowance will be active but will not be displayed.
- An infrequent inability to park to a frozen master time code is corrected.

PARTS REQUIRED

Part No.	Description	Qty.
EAR-5003V-01	P4.02.01.5 / P1.01.01.3 Firmware Kit	1